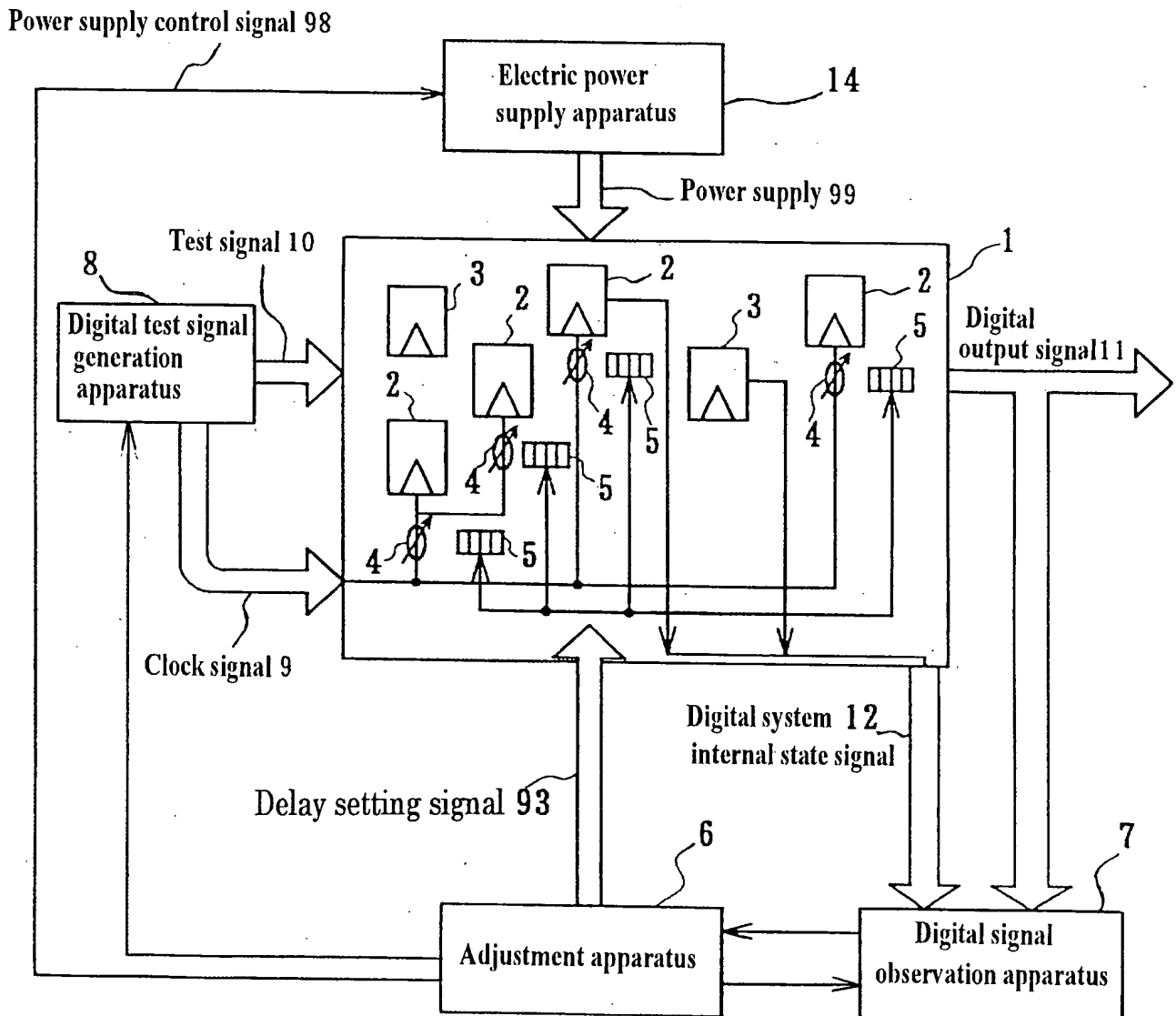


FIG. 1



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FIG. 2

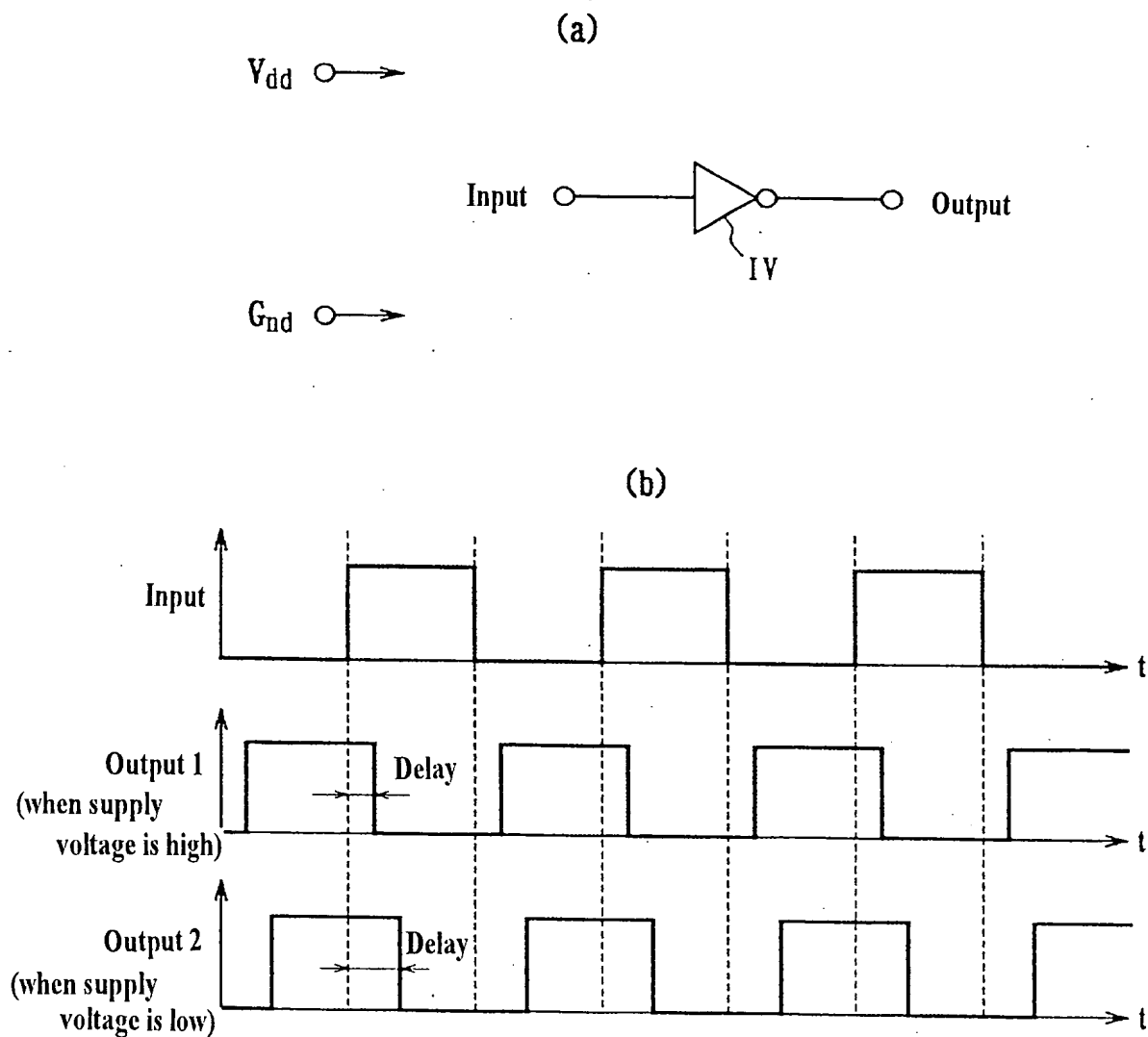
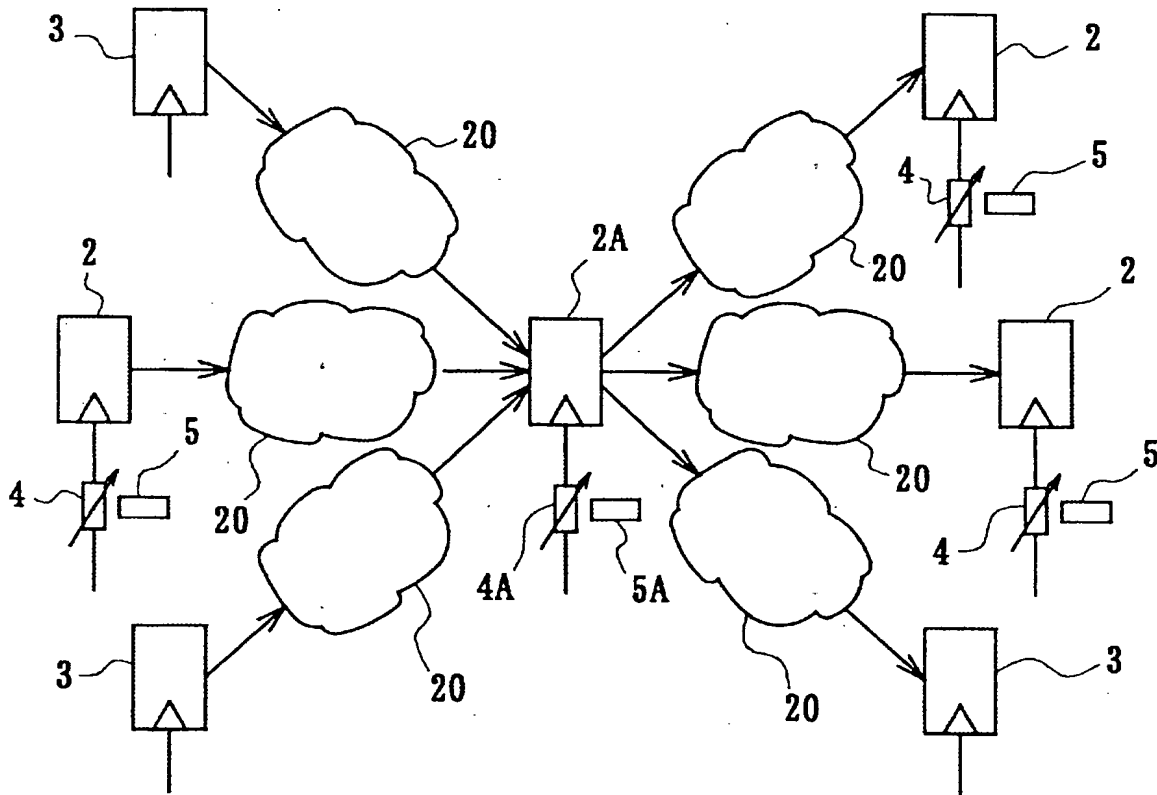


FIG. 3



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FIG. 4

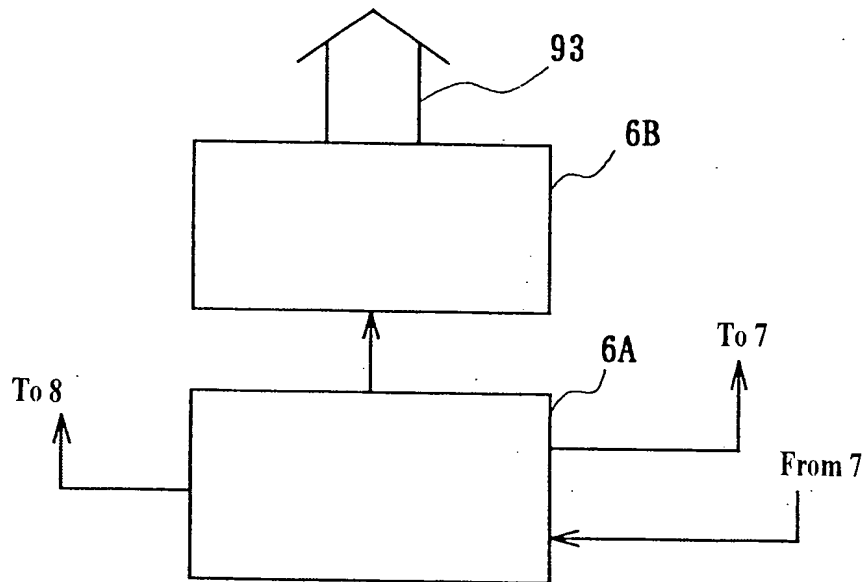


FIG. 5

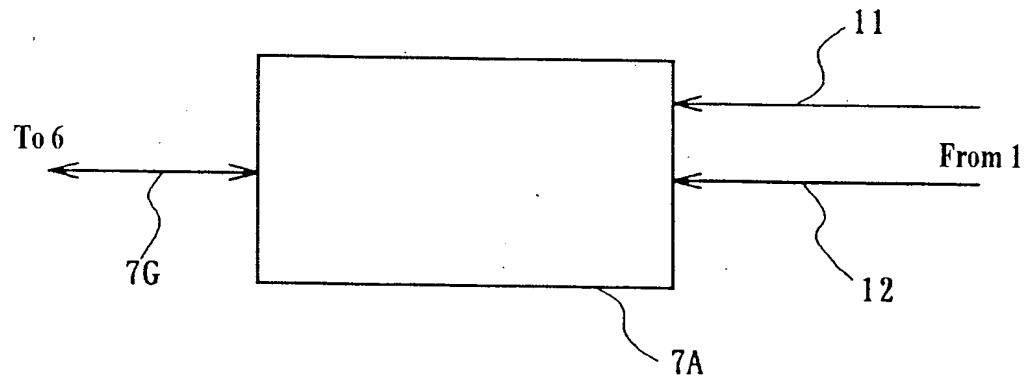
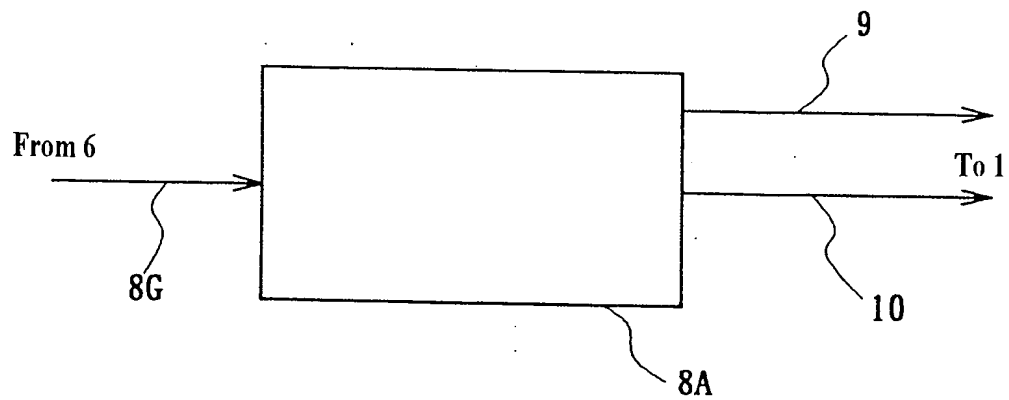
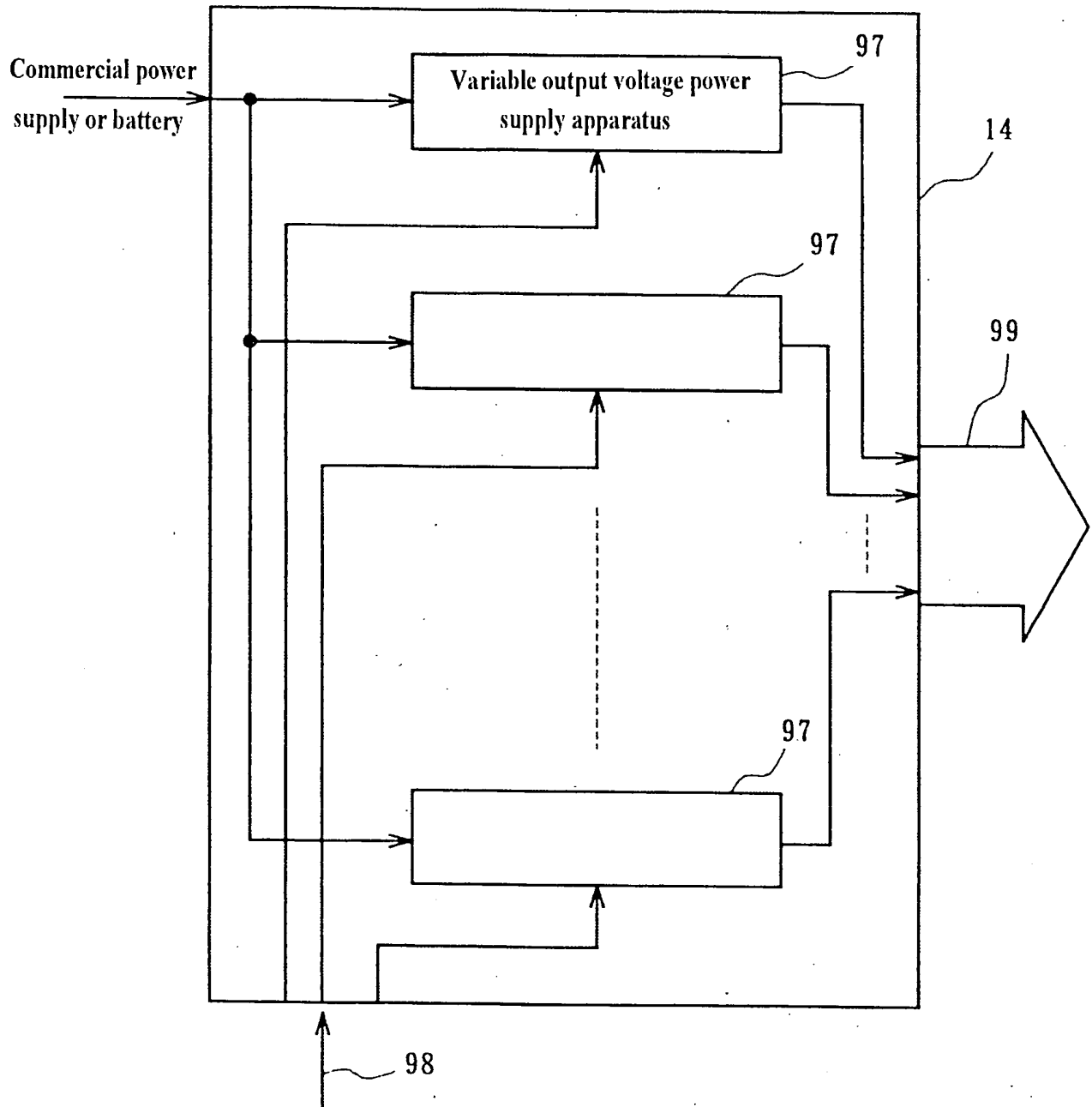


FIG. 6



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FIG. 7



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FIG. 8

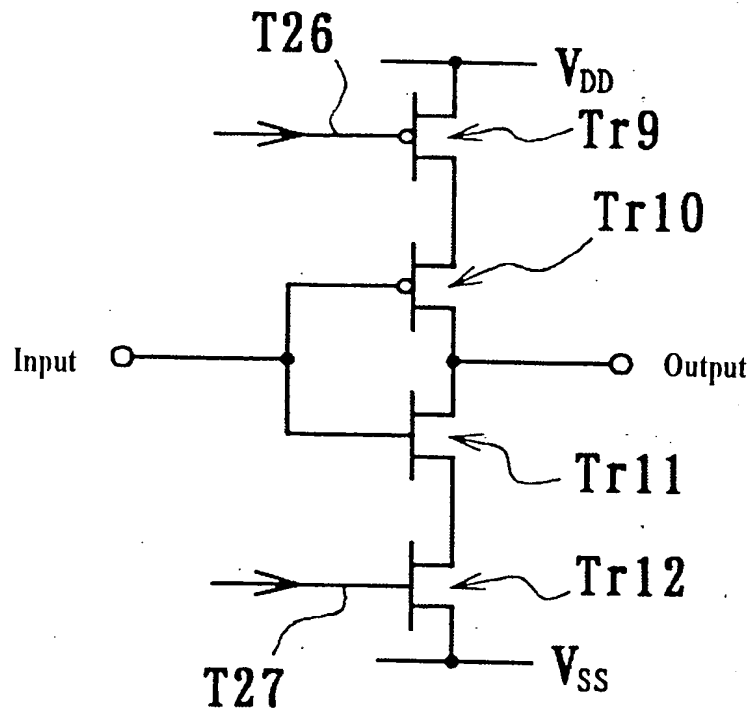
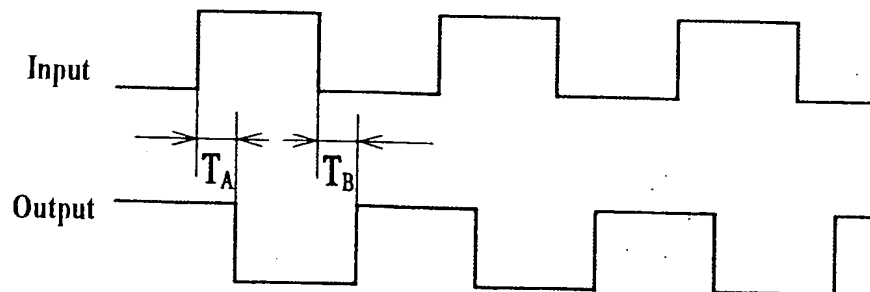


FIG. 9



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FIG. 10

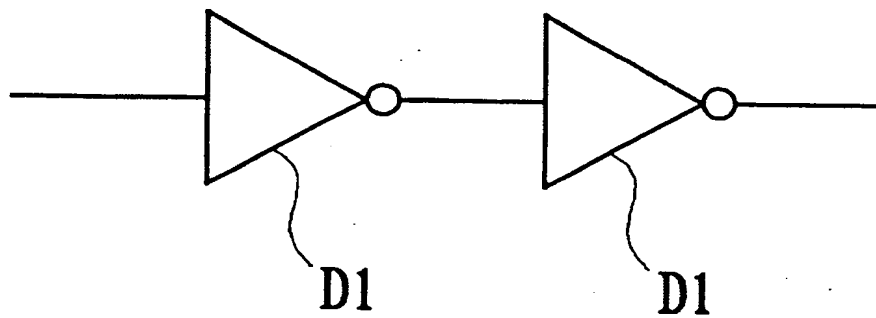


FIG. 11

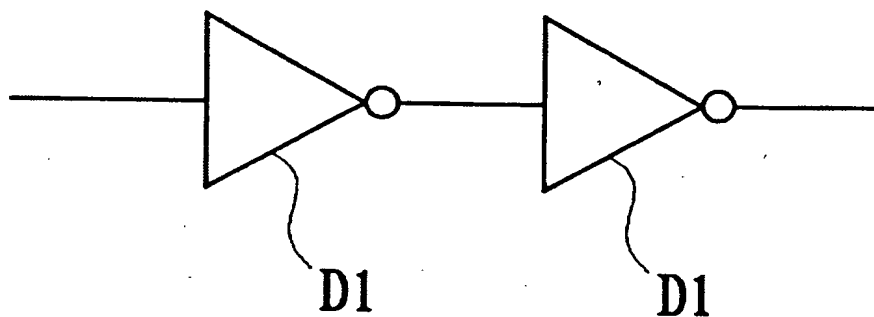


FIG. 12

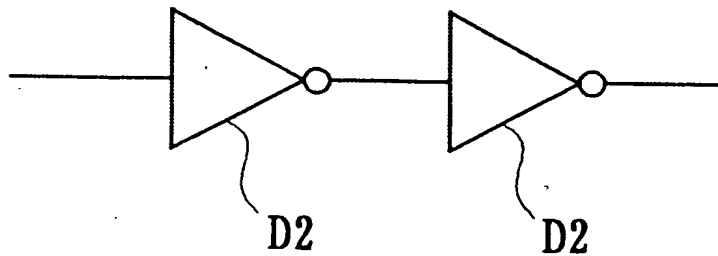


FIG. 13

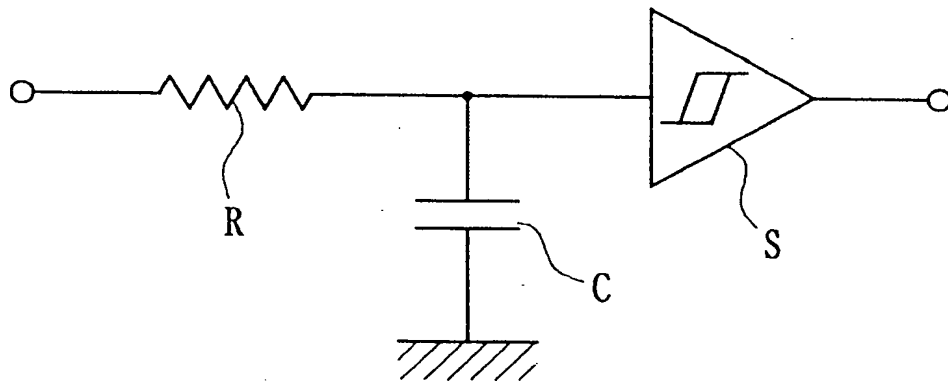


FIG. 14

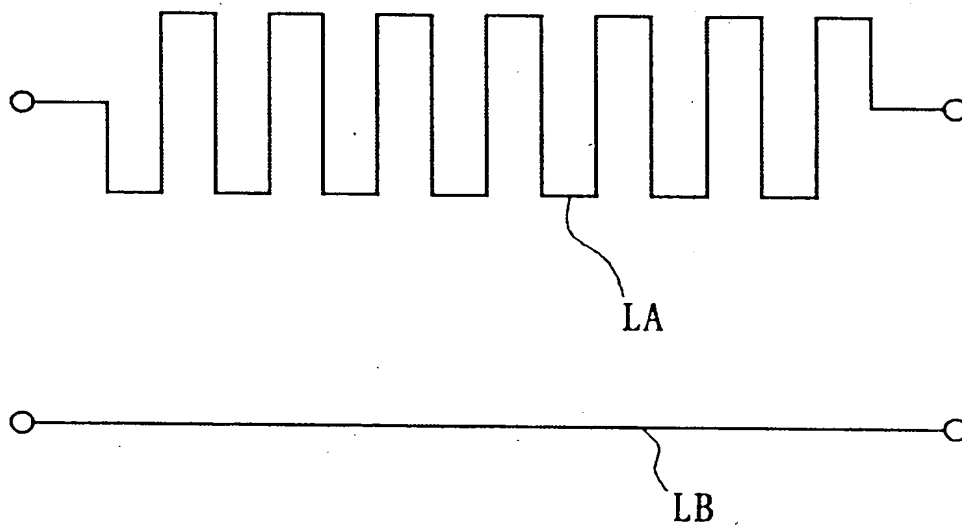
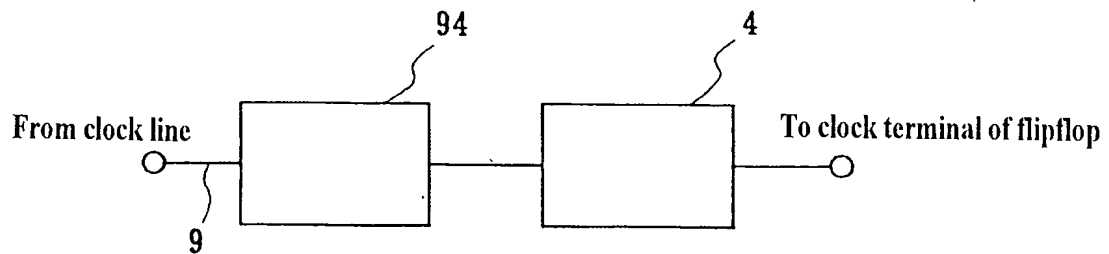


FIG. 15



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FIG. 16

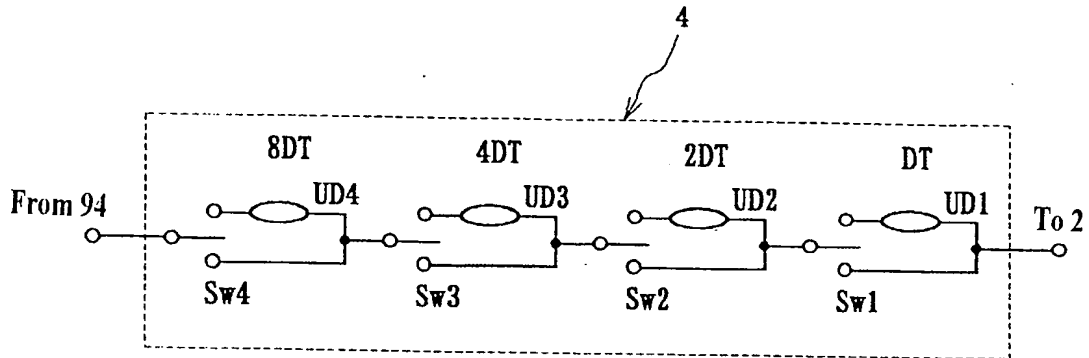
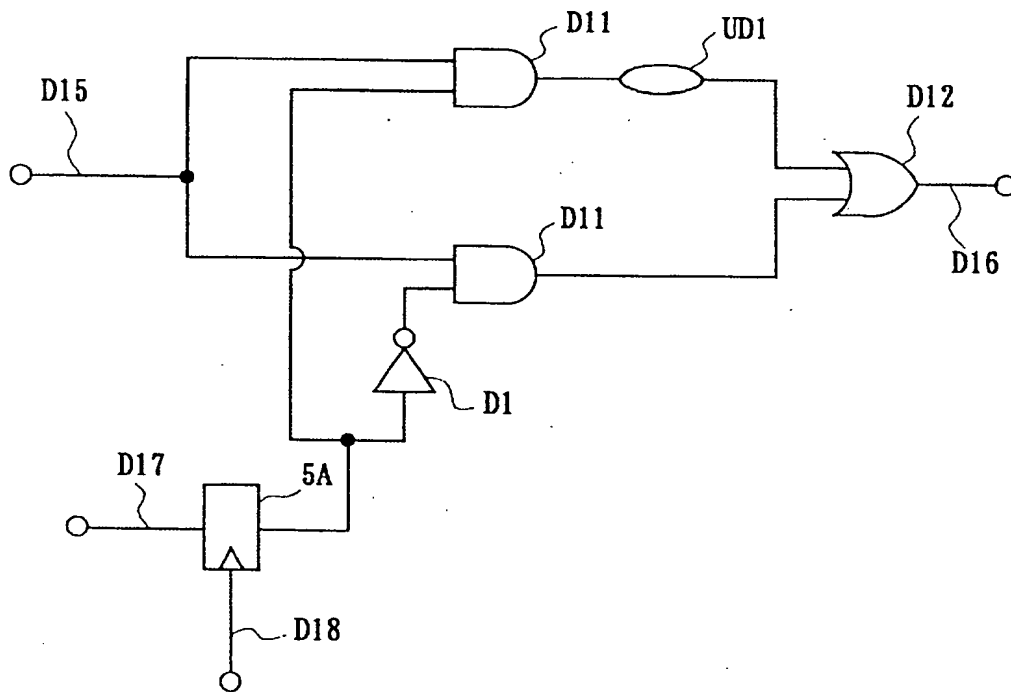
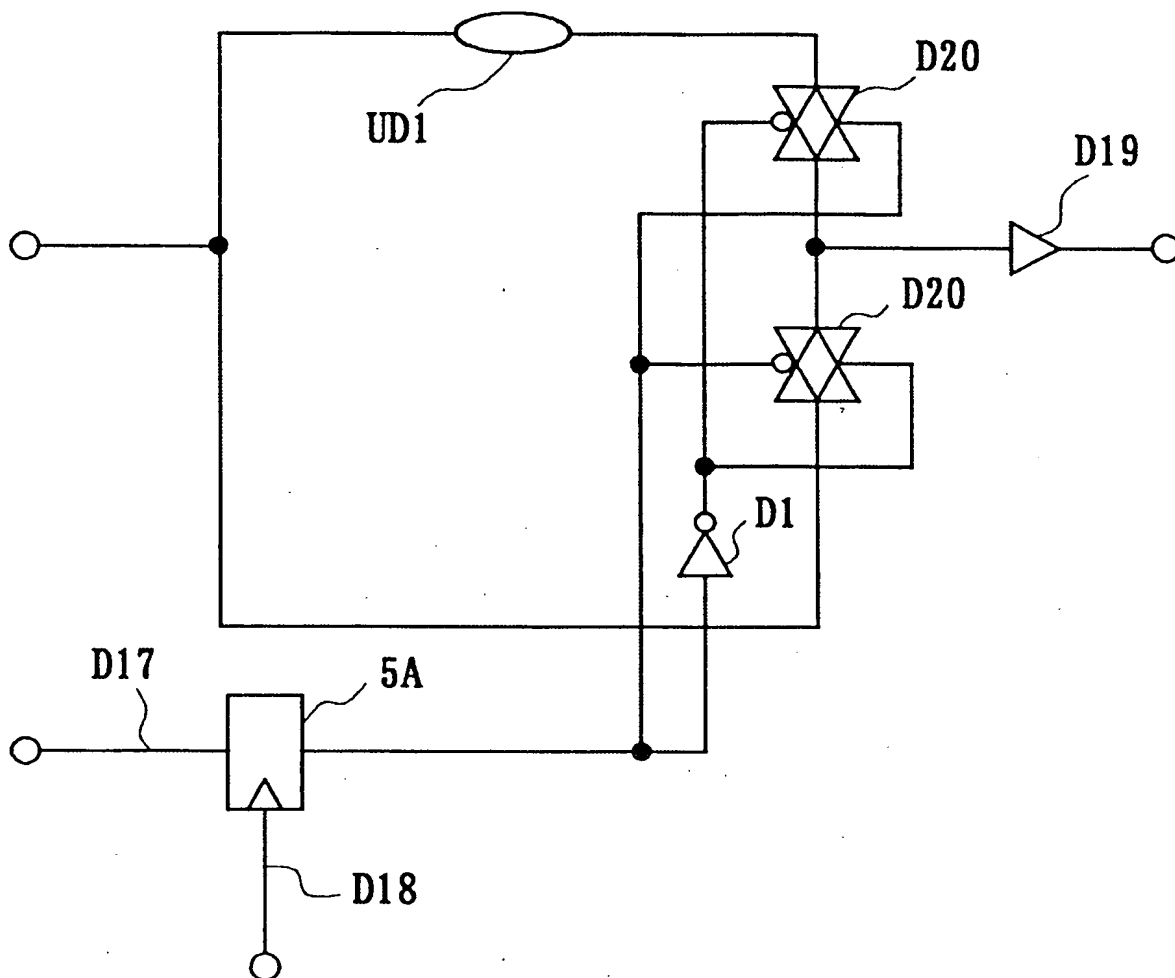


FIG. 17



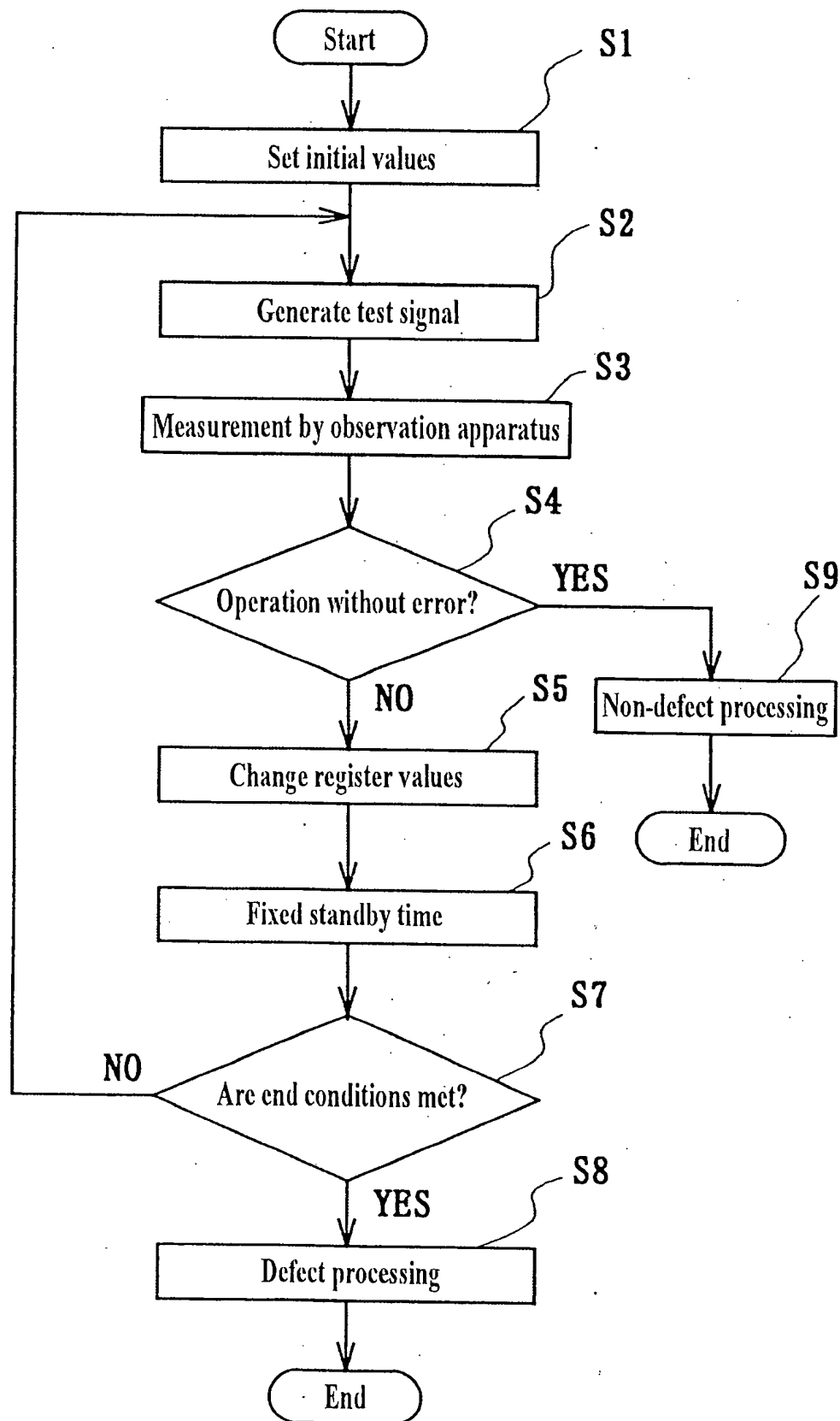
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FIG. 18



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FIG. 19



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FIG. 20

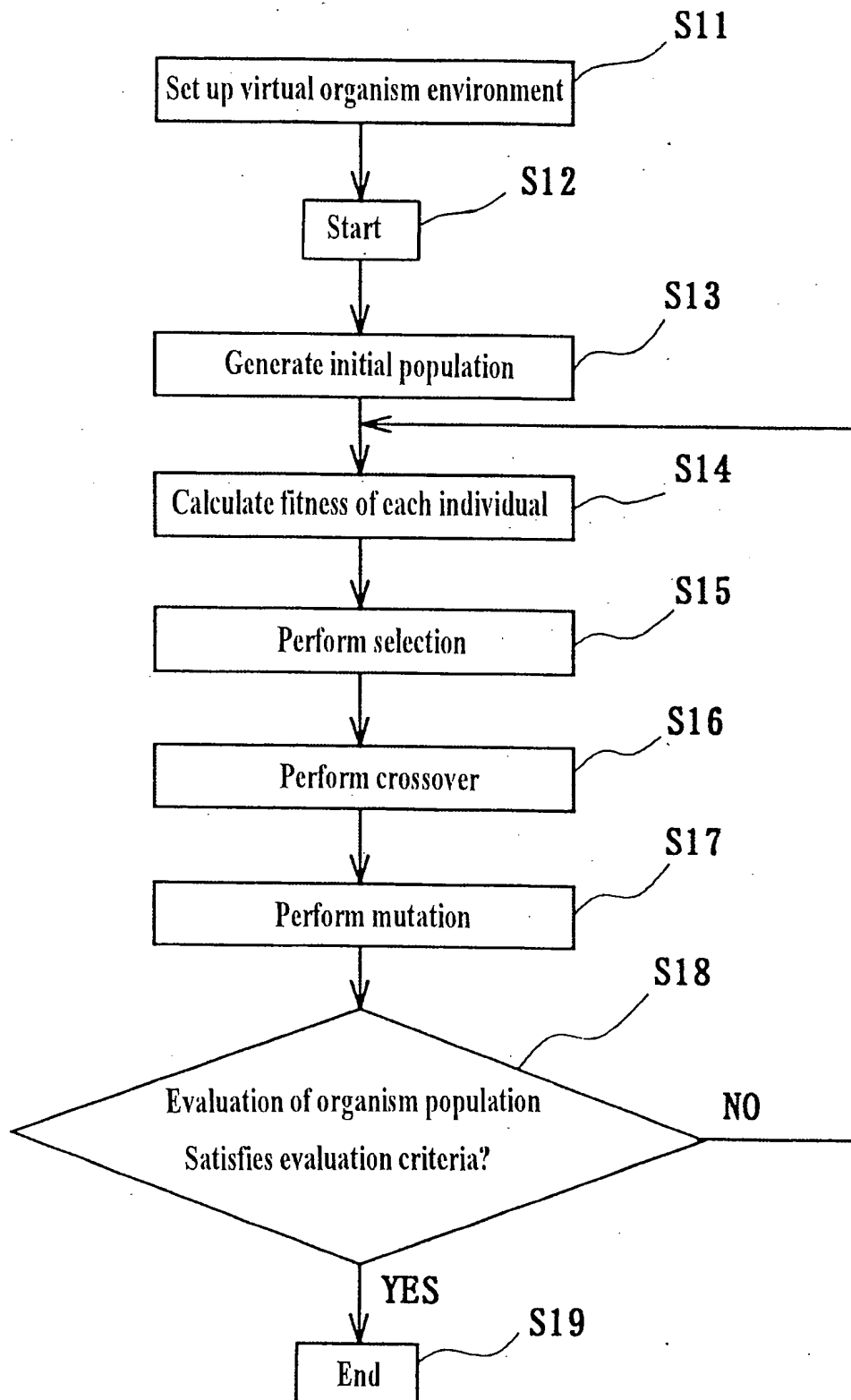
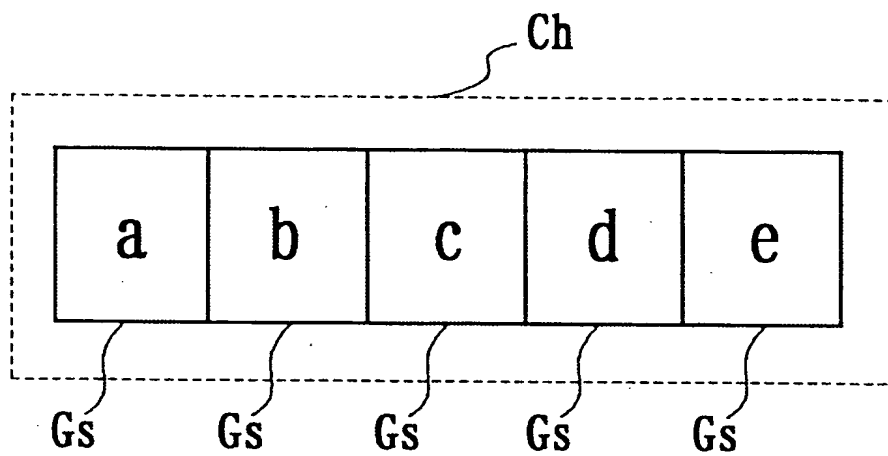
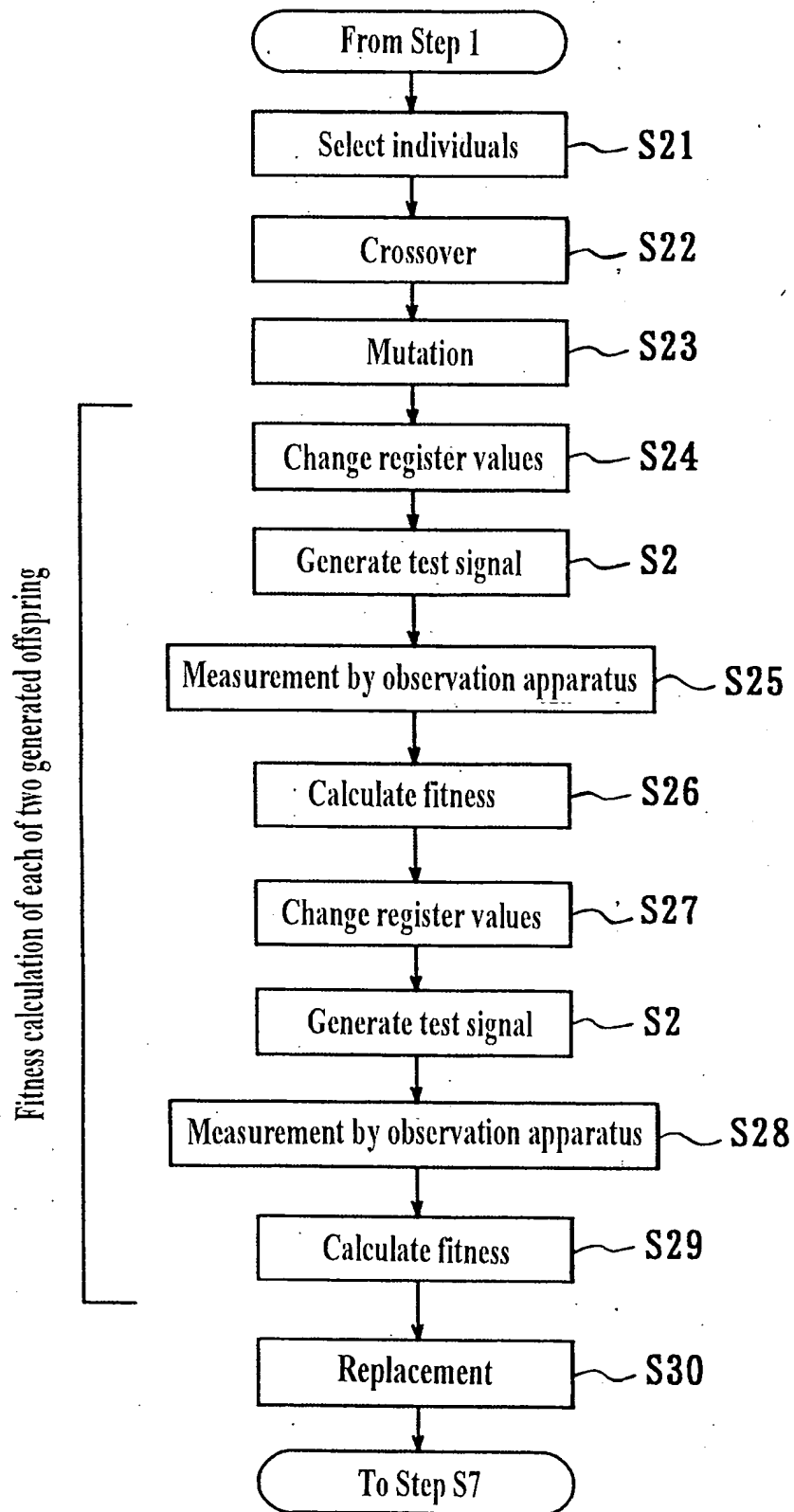


FIG. 21



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FIG. 22



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FIG. 23

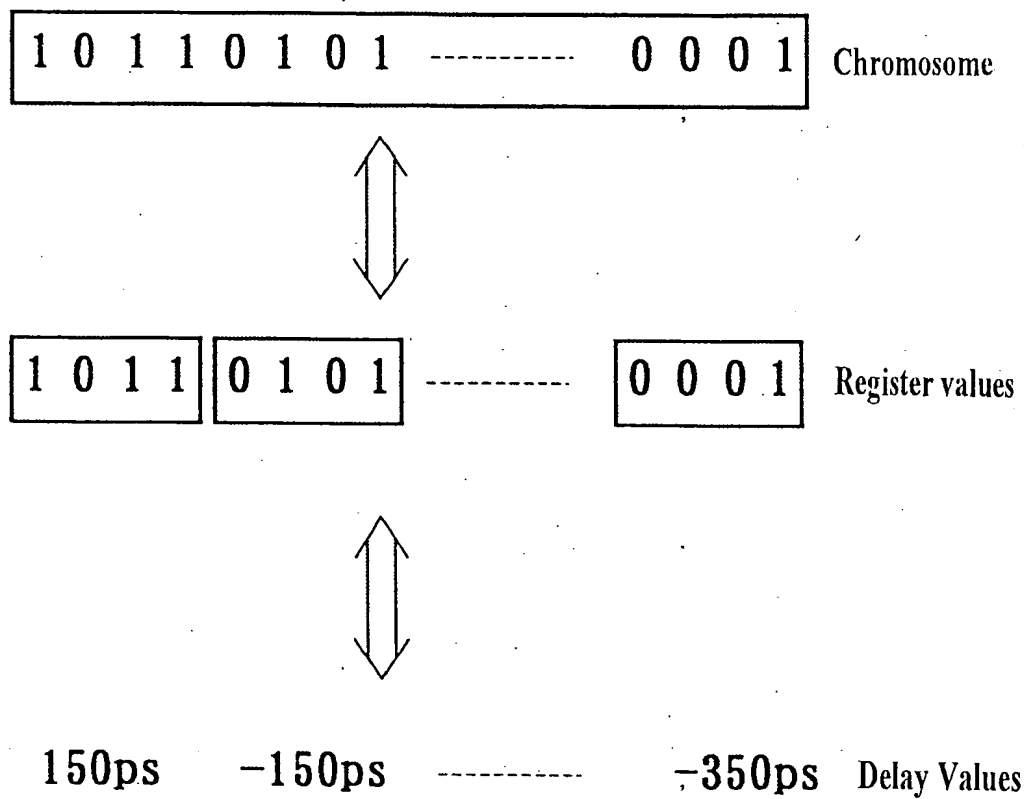
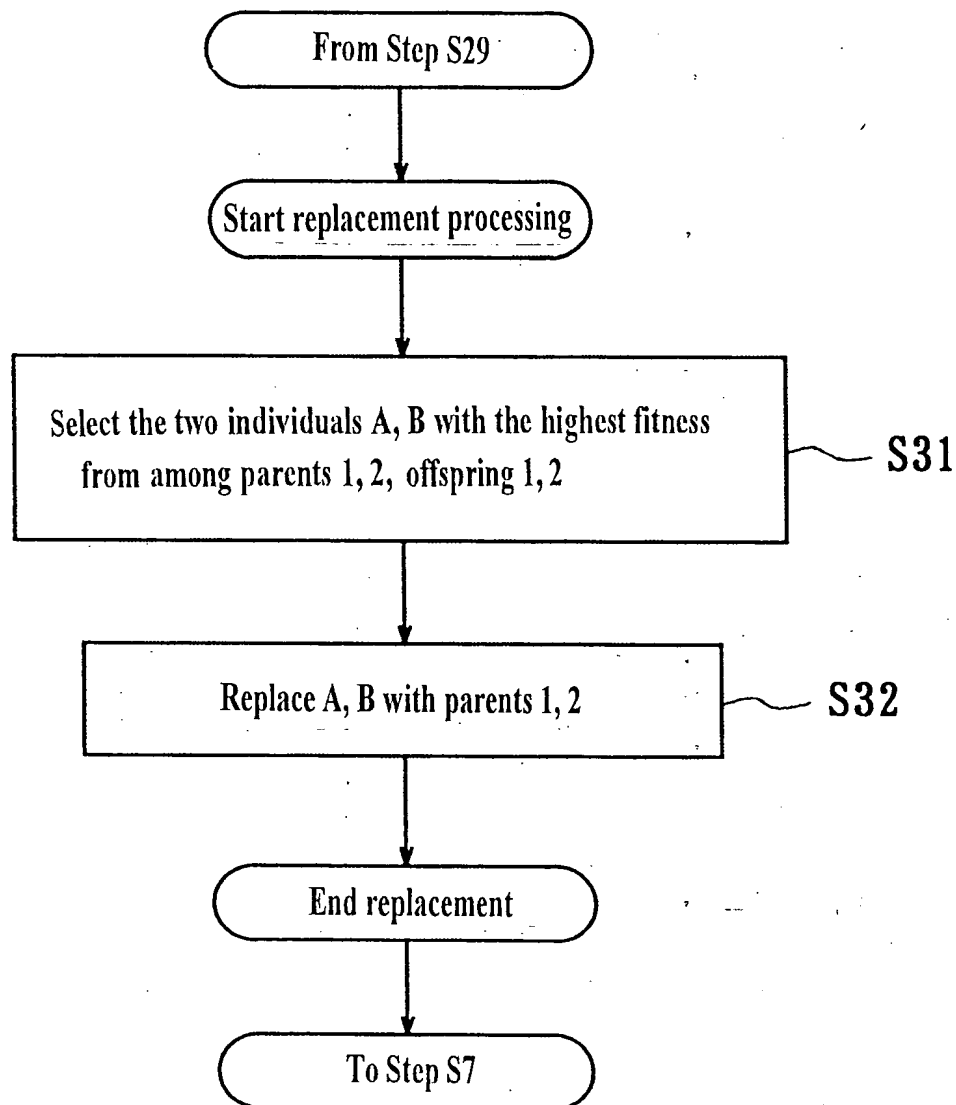


FIG. 24



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FIG. 25

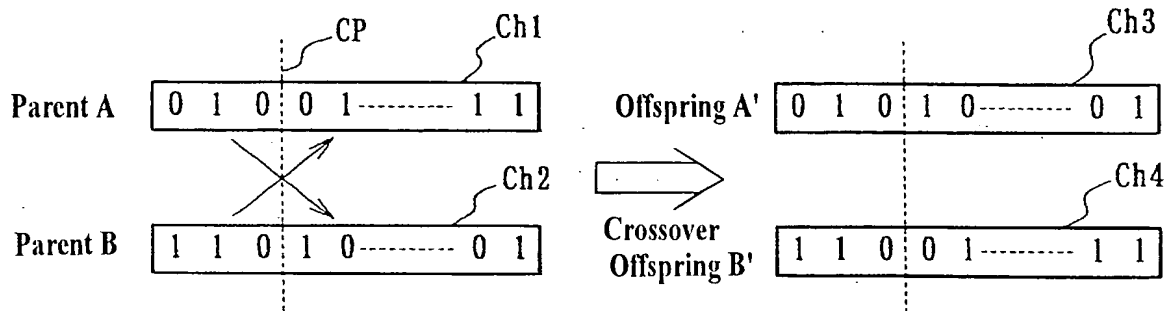


FIG. 26

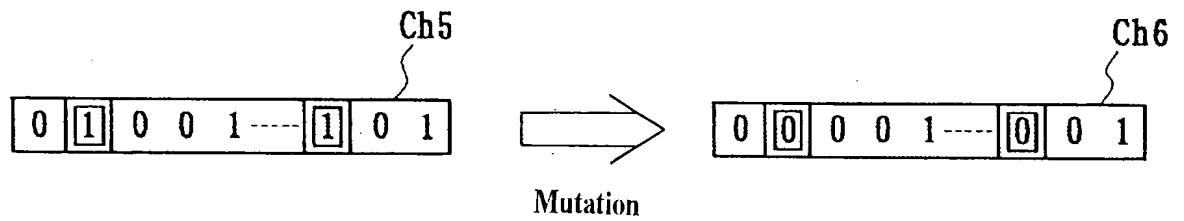


FIG. 27

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(Supply voltage value $V_0 > V_1 > \dots > V_n$)

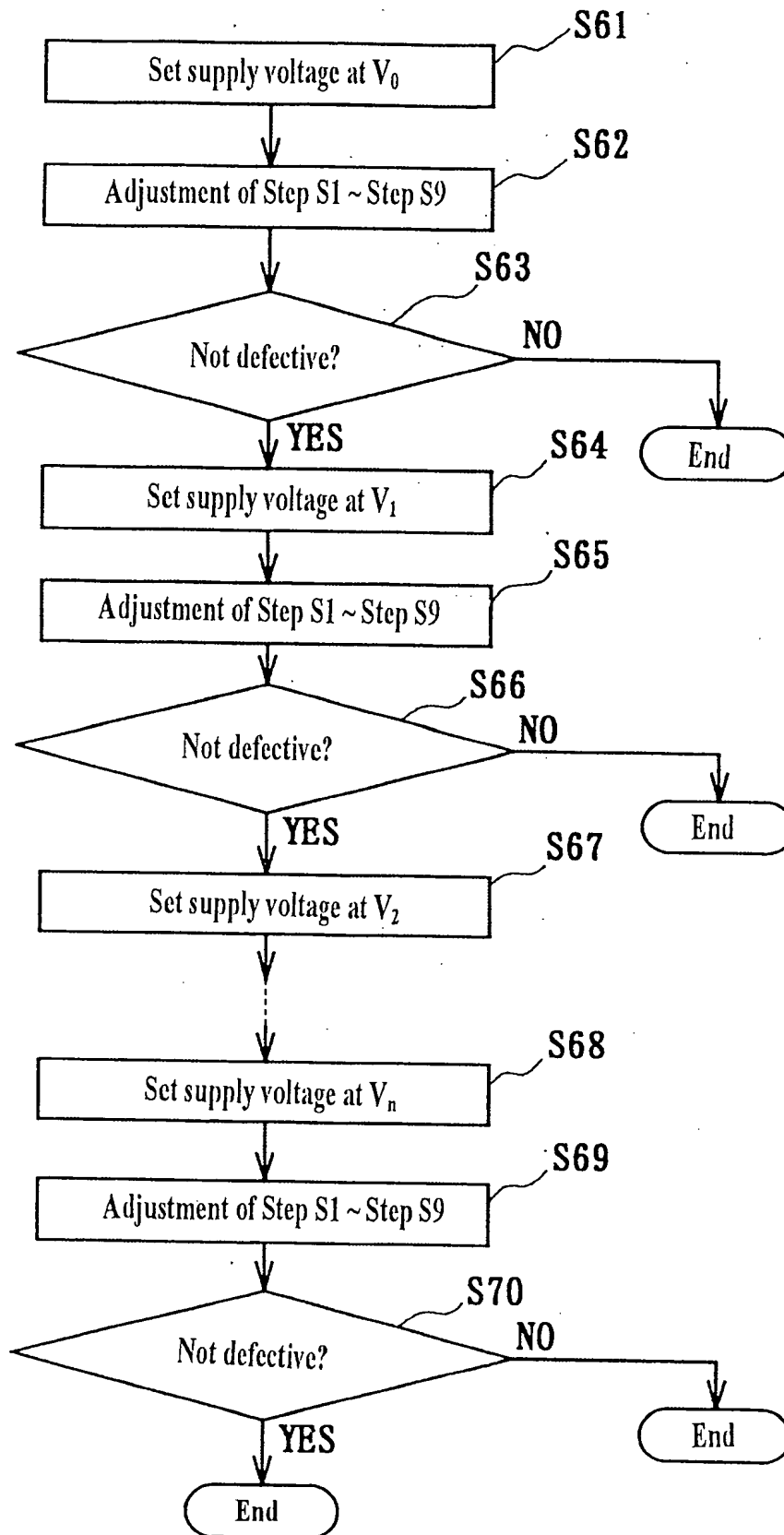
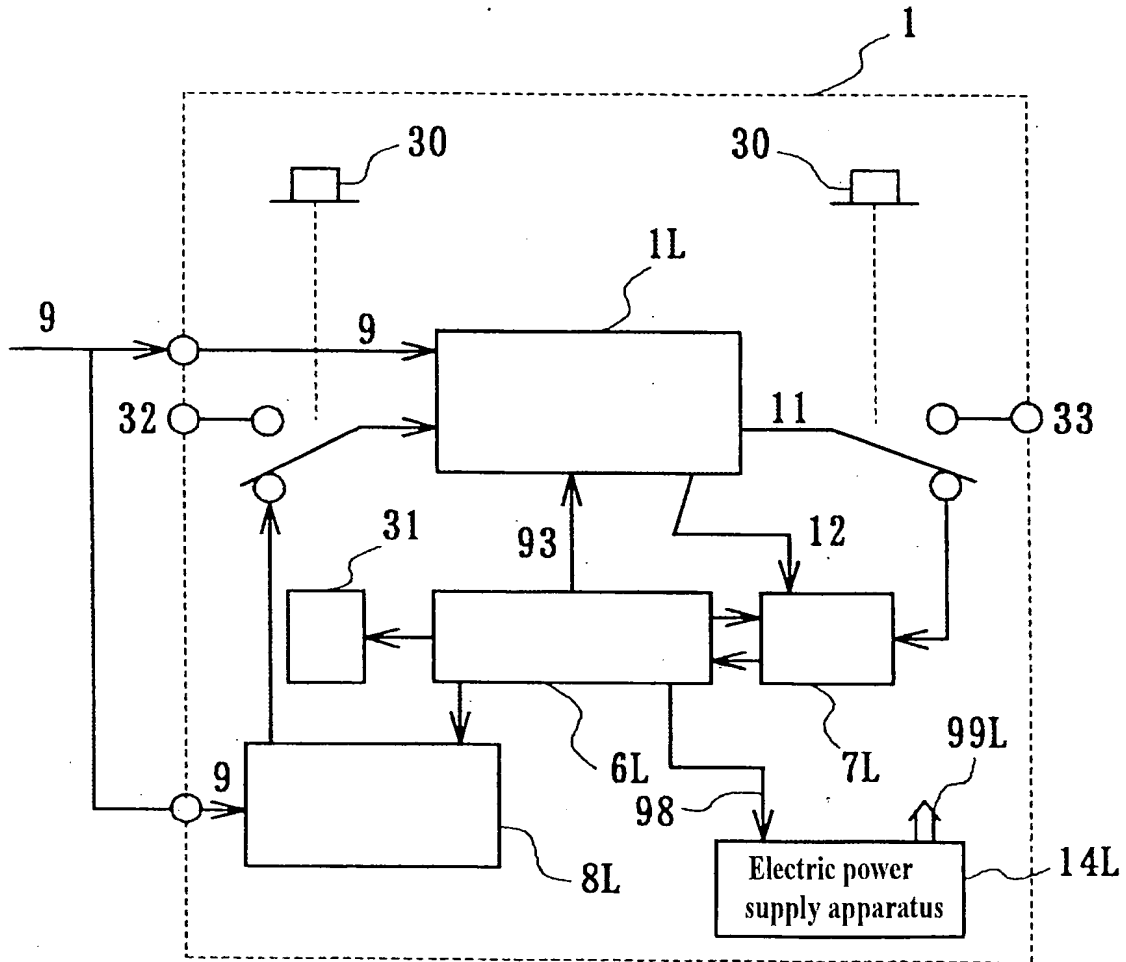
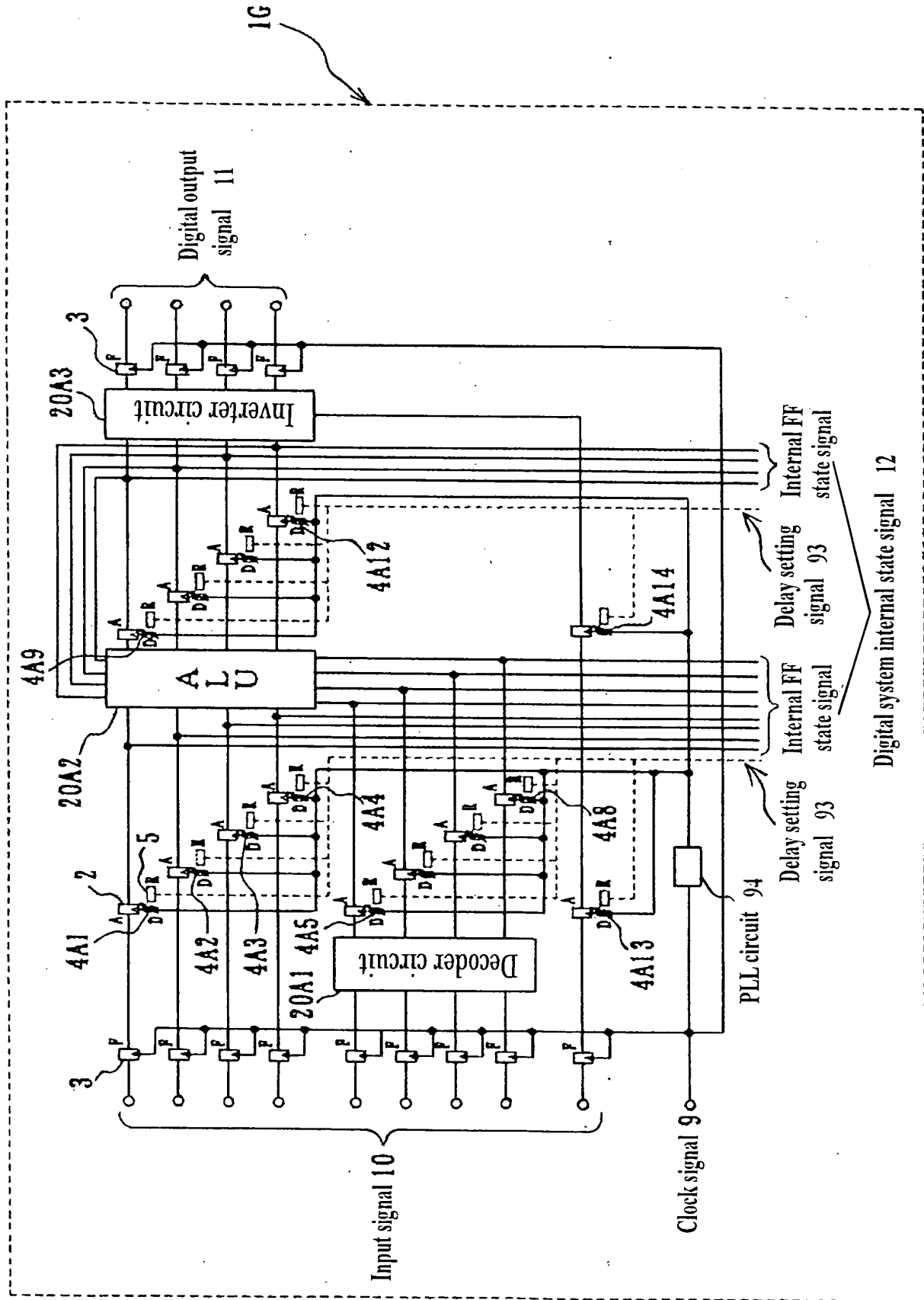


FIG. 28



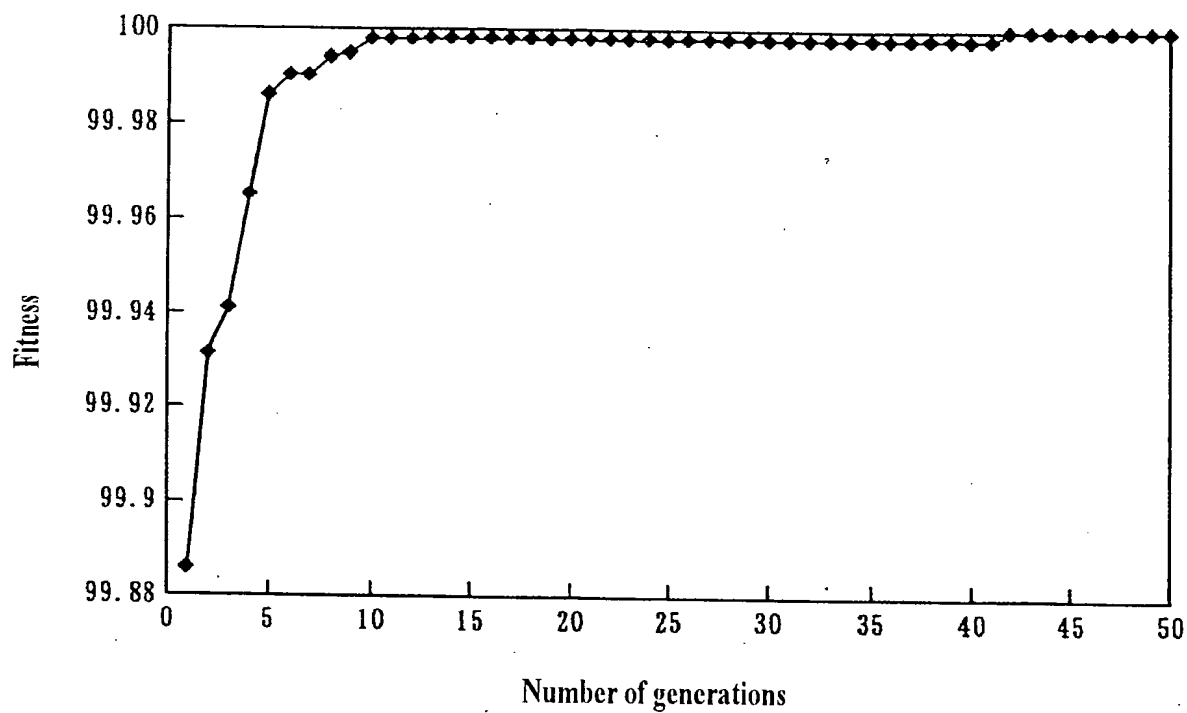
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FIG. 29



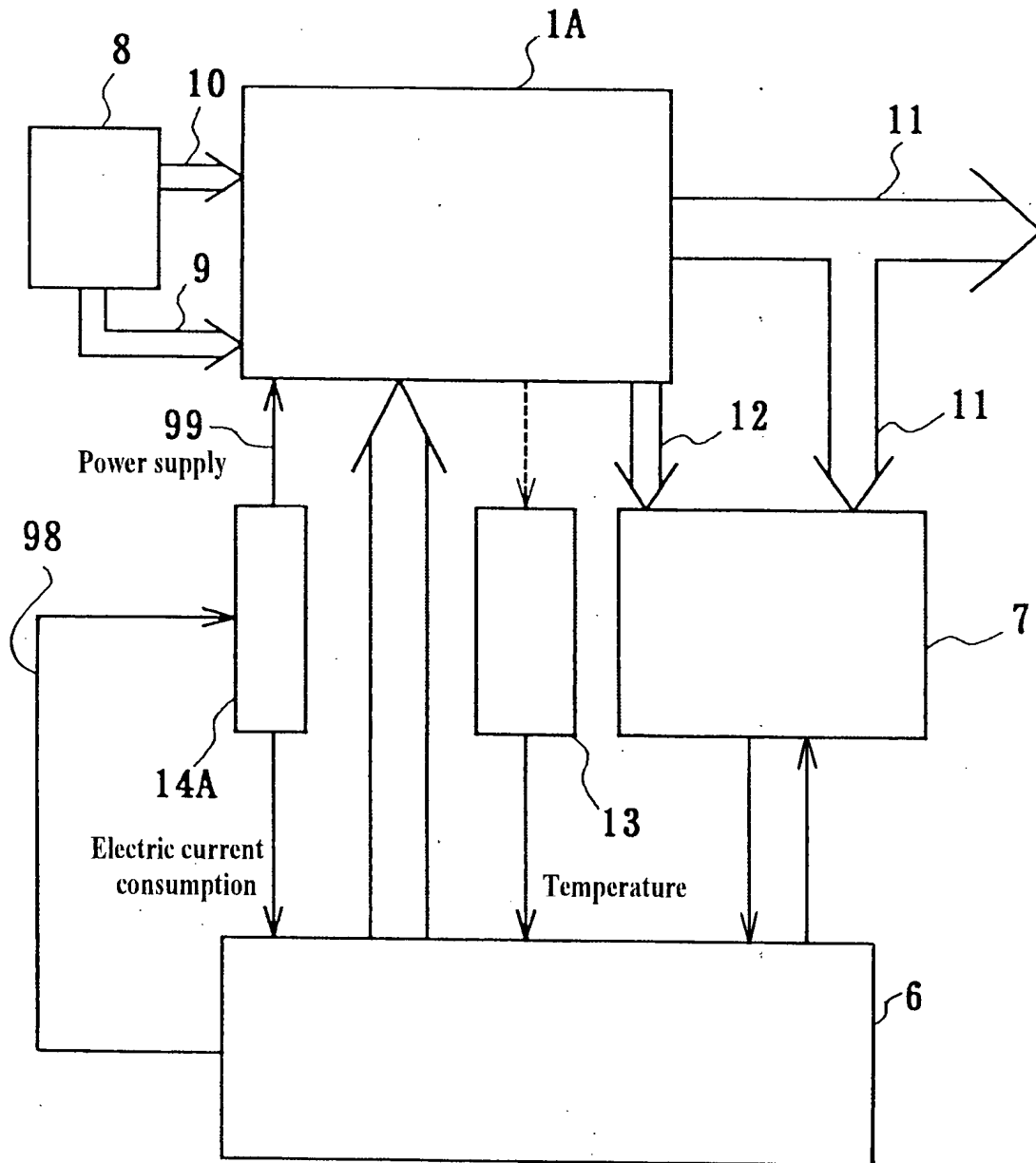
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FIG. 30



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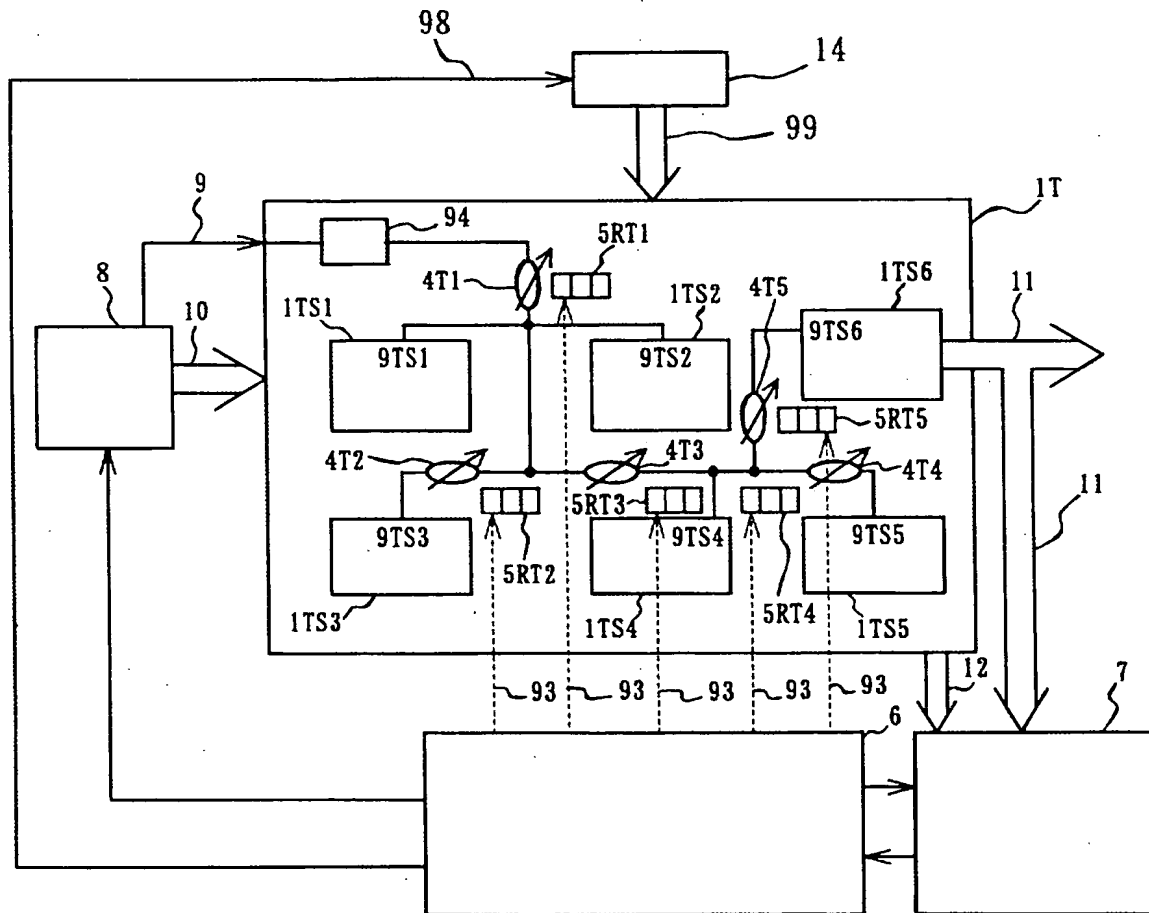
FIG. 31



[illegible]

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FIG. 33



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FIG. 34

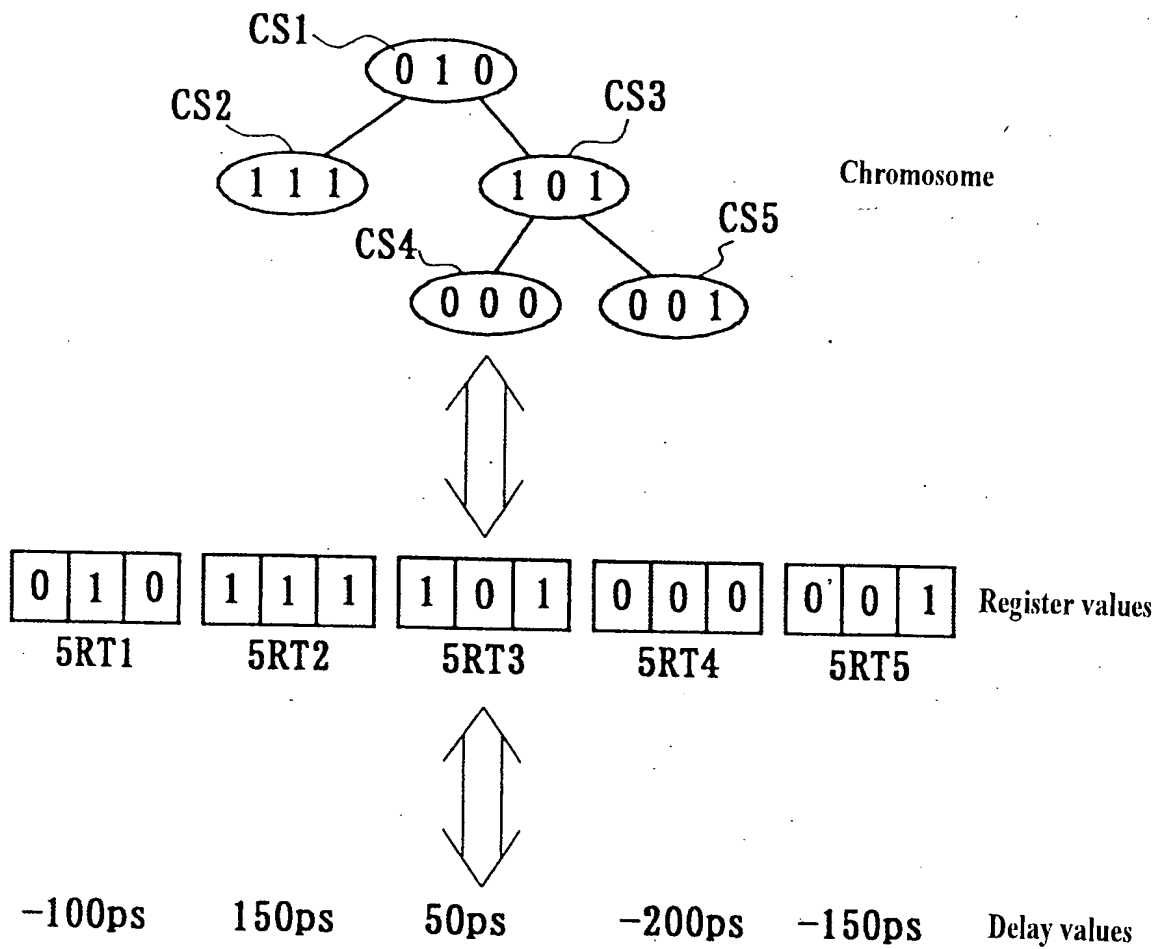


FIG. 35

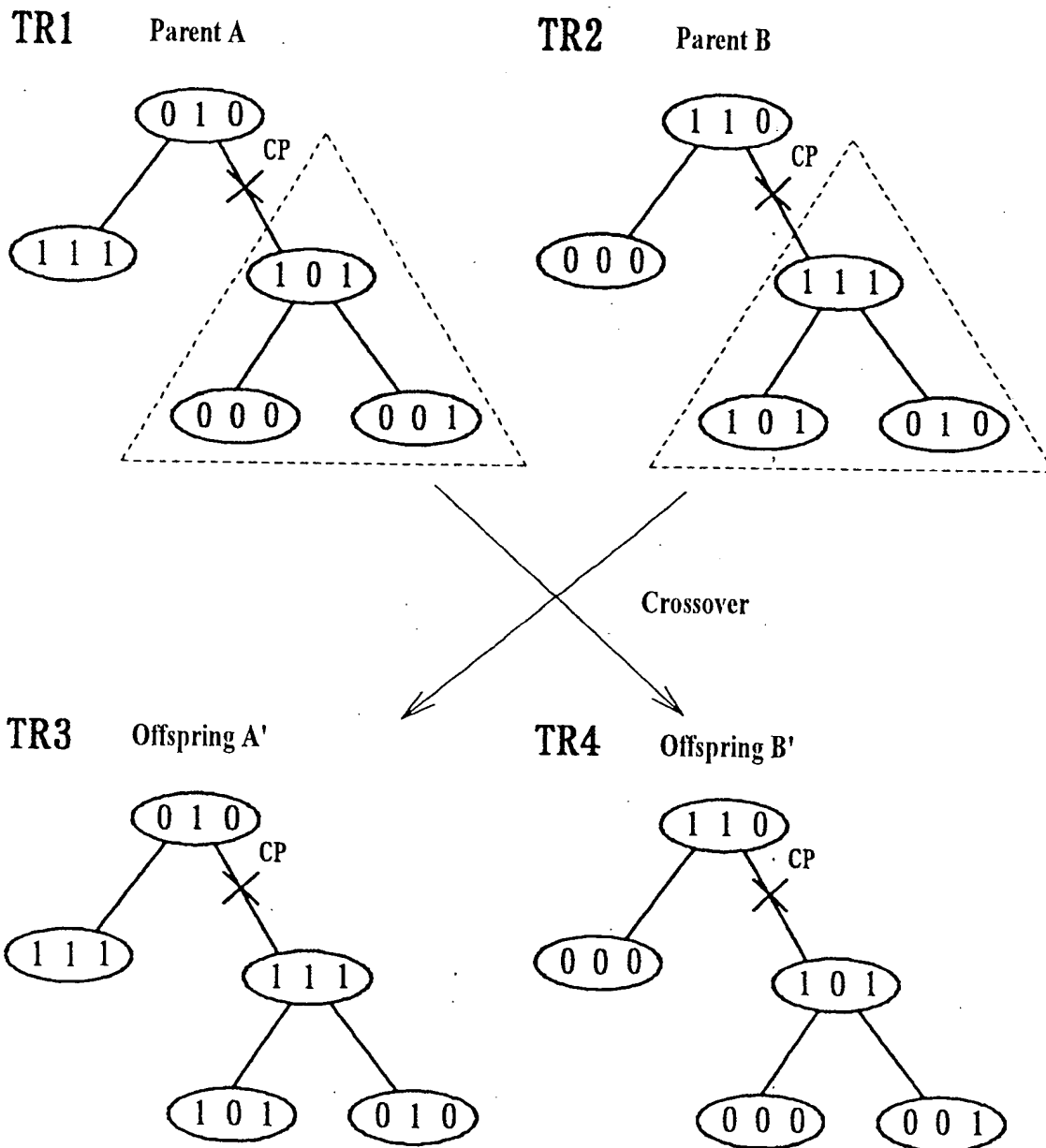


FIG. 36

